Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L34	1	(output and (control controller) and (switch switching) and current and (reduce ramp down lower) and surge and rate and (processor processing) and (accelerate increase) and signal).clm.	US-PGPUB	OR	ON	2008/01/11 16:51
L35	4	(output and (control controller) and (switch switching) and current and (reduce ramp down lower) and surge and rate and (processor processing) and signal).clm.	US-PGPUB	OR	ON	2008/01/11 16:53
L36	4	(power and output and (control controller) and (switch switching) and current and (reduce ramp down lower) and surge and rate and (processor processing) and signal). clm.	US-PGPUB	OR	ON	2008/01/11 16:53
L37	8	(power and output and (control controller) and (switch switching) and current and (reduce ramp down lower) and surge and rate and signal).clm.	US-PGPUB	OR	ON	2008/01/11 16:53

Ref	Hits	Search Query	DBs	Default	Plurals	Time Stamp
#				Operator		2000/04/44 45 20
L1	3	"7116011".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:08
L2		307/59.ccls. and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:08
L3	.0	307/52.ccls. and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:09
L4	1	307/98.ccls. and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11.16:10
L5	1	307/103.ccls. and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:10
L6	30	(processor cpu computer) same shot same output and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:11
L7	8	307/140.ccls. and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2008/01/11 16:12

L8	110	713/300.ccls. and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR ·	ON	2008/01/11 16:13
L9	57	713/320.ccls. and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:14
L10	2618	((switching near2 (regulator converter)) or (battery near (charg\$4 discharg\$4 recharg\$4)) or ((dc "dc-dc" "dc-to-dc" "dc/dc") near2 converter)) and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:15
L11	3	((switching near2 (regulator converter)) or (battery near (charg\$4 discharg\$4 recharg\$4)) or ((dc "dc-dc" "dc-to-dc" "dc/dc") near2 converter)) and (zener with reference with comparator) and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:16
L12	436	(cpu computer) and ((surge overvoltage over\$2voltage (voltage near3 high)) with signal) and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:16
L13	30	(processor cpu computer) same shot same output and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:19
L14	436	(cpu computer) and ((surge overvoltage over\$2voltage (voltage near3 high)) with signal) and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:19

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L15	3	(cpu computer) and ((surge overvoltage over\$2voltage (voltage near3 high)) with signal) and one\$2shot and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:20
L16		processor same surge same (notification adj signal) same (current with (ramp ramp-down (ramp adj down))) and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:20
L17		transient adj notification and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:20
L18	3	surge same (sleep adj mode) and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:21
L19	261	(processor cpu computer) same shot and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:21
L20	3	(processor cpu computer) same shot same timer and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:22
L21	0	(processor cpu computer) same shot same timer same output and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2008/01/11 16:22

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L22	2	nguyen-don.in. and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:22
L23	1	waizman-alex.in. and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:23
L24	436	(cpu computer) and ((surge overvoltage over\$2voltage (voltage near3 high)) with signal) and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:24
L25	0	processor same surge same (notification adj signal) same (current with (ramp ramp-down ramp\$down)) and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:25
L26	0	surge adj notification and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:25
L27		transient adj notification and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:25
L28	3	surge same (sleep adj mode) and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:27

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L29	53050	(processor cpu computer) and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:27
L30	261	(processor cpu computer) same shot and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:27
L31	3	(processor cpu computer) same shot same timer and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/11 16:28
L32	0	(processor cpu computer) same shot same timer same output and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR ·	ON	2008/01/11 16:28
L33		(processor cpu computer) same shot same timer same ramp and @pd>="20071102"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2008/01/11 16:28

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